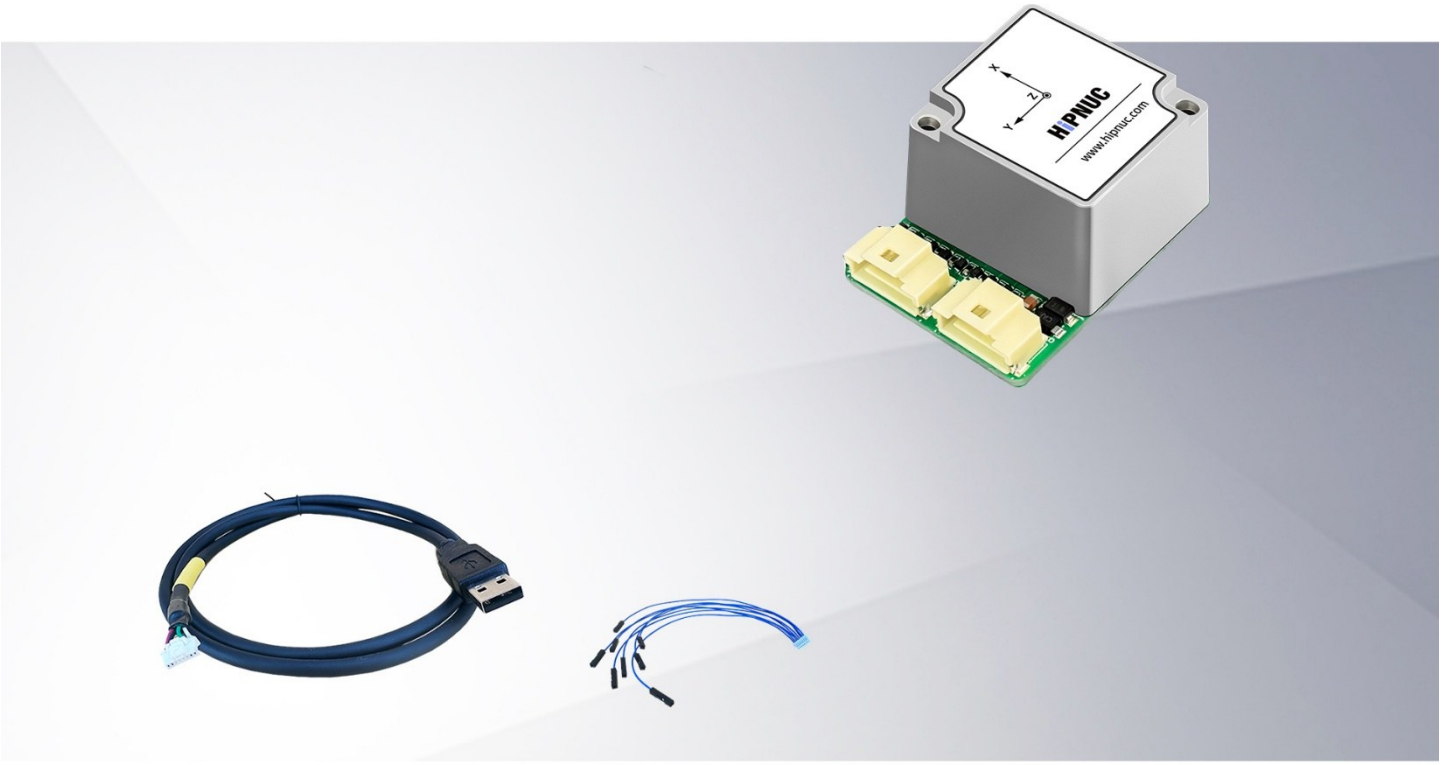

Document Information

Version	Date	Author	Description
1.0	July 28, 2025	HiPNUC	Initial release
1.1	April 23, 2026	HiPNUC	Updated appearance and pin definitions

Contents

1 Overview

The EVAL HI71 is an evaluation board designed for HI71 series modules, enabling rapid functional verification and performance evaluation. It is equipped with two Molex connectors, J1 and J2, and can communicate with a host through a USB-to-Molex cable assembly or an open-end cable assembly. To ensure stable test results, the EVAL HI71 should be securely mounted to the user equipment or test fixture during operation.

2 Ordering Information

2.1 Model Information

Table 1: Model Information

Part Number	Name	Description
EVAL HI71T2-MI0	HI71T2-MI0 Evaluation Board	Evaluation board for HI71T2-MI0
EVAL HI71T4-MI0	HI71T4-MI0 Evaluation Board	Evaluation board for HI71T4-MI0
EVAL HI71M4-MI0	HI71M4-MI0 Evaluation Board	Evaluation board for HI71M4-MI0
EVAL HI71N4-MI0	HI71N4-MI0 Evaluation Board	Evaluation board for HI71N4-MI0

2.2 Contact Information

Email: overseas1@hipnuc.com

Website: www.hipnuc.com

3 Related Documents

1. HI71 Datasheet
2. Command and Programming Manual
3. STEP File
4. GUI Software and Reference Examples

4 Specifications

4.1 Absolute Maximum Ratings

The recommended power supply range is **3.6 V to 5.5 V**. Exceeding the absolute maximum ratings may cause permanent damage to the device.

Table 2: Absolute Maximum Ratings

Parameter	Limit	Description
Mechanical Shock	3000 g	Duration < 0.5 ms
Storage Temperature	-40 °C to 85 °C	
ESD (HBM)	2 kV	JEDEC/ESDA JS-001
Input Voltage (VDD)	9.0 V	
I/O Pin Voltage to GND	-0.3 V to 3.6 V	

4.2 Mechanical Dimensions and Pin Definitions

All dimensions are in mm.

4.2.1 EVAL HI71 Dimensions

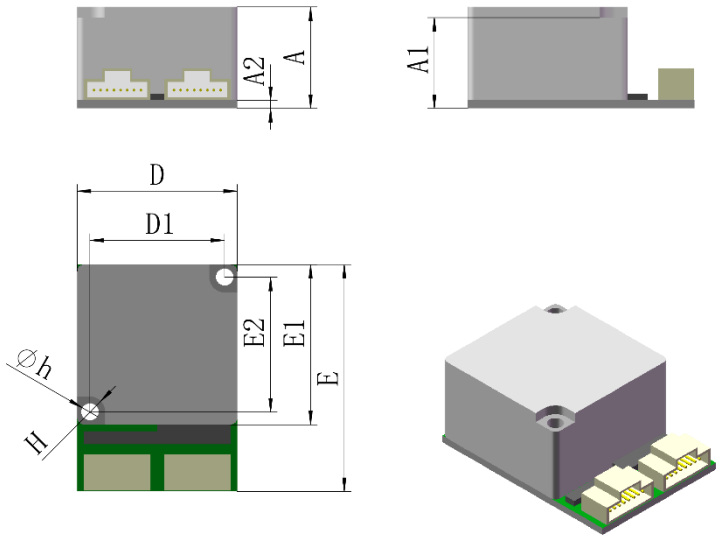

Figure 1: HI71 Evaluation Board Dimensions

Table 3: HI71 Evaluation Board Dimensions

Symbol	Min (mm)	Typ (mm)	Max (mm)
D	23.8	24	24.2
D1	20.1	20.2	20.3
E	33.8	34	34.2
E1	23.9	24	24.1
E2	20.1	20.2	20.3
A	15	15.2	15.4
A1	13.8	14	14.2
A2	1.5	1.6	1.7
H	R2.2	R2.3	R2.4
h	Φ2.5	Φ2.6	Φ2.7

4.2.2 EVAL HI71 Pin Definitions

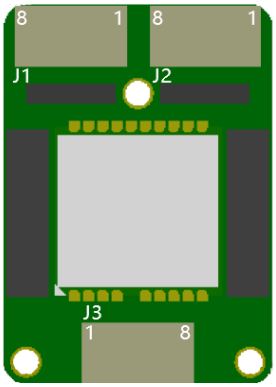

Figure 2: EVAL HI71 Pin Assignment

Table 4: J1 Pin Description

Pin Number	Pin Name	Description
1	UART1_TX	UART1 transmit
2	UART1_RX	UART1 receive
3	GND	Power ground
4	GND	Power ground
5	IO1/SYNC_IN/PPS	Synchronization input. Can accept an external trigger signal, such as a GNSS PPS signal.
6	IO2/SYNC_OUT	Synchronization output. Can be used as a Data Ready signal.
7	GND	Power ground
8	VDD	Power input, 3.6 V to 5.5 V

Table 5: J2 Pin Description

Pin Number	Pin Name	Description
1	UART2_TX	UART2 transmit
2	UART2_RX	UART2 receive
3	NC	Reserved, leave unconnected
4	NC	Reserved, leave unconnected
5	UART3_RX	UART3 receive
6	UART3_TX	UART3 transmit
7	GND	Power ground
8	VDD	Power input, 3.6 V to 5.5 V

5 Cable Assemblies

5.1 Molex A (501330-0800) to 2.54 mm Header (Dupont) Cable Assembly

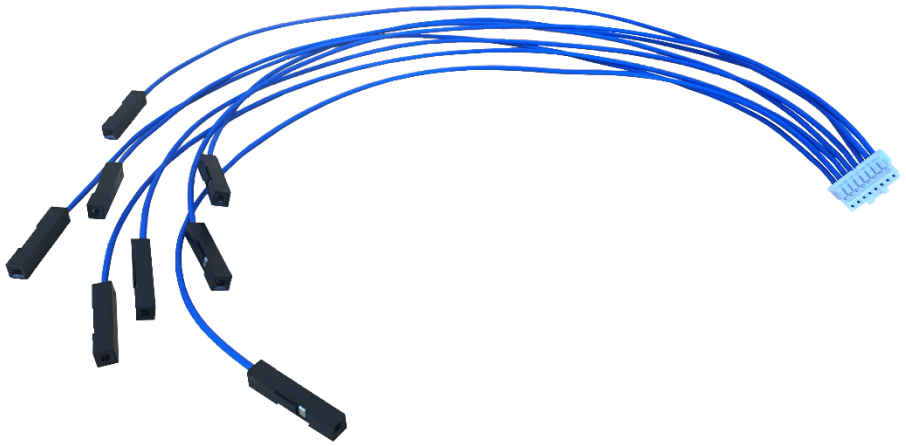

Figure 3: Molex A (501330-0800) to 2.54 mm Header (Dupont) Cable Assembly

Note 1: Cable length: 30 cm

5.2 USB to Molex A (501330-0800) Cable Assembly

Figure 4: USB to Molex A (501330-0800) Cable Assembly

Note 1: This cable assembly is intended for HI71 series evaluation boards. The cable length is 1 m, and it integrates a USB-to-UART (TTL-level) converter.

Note 2: Driver download: CP210x USB to UART Bridge VCP Drivers